0070271 "4422260

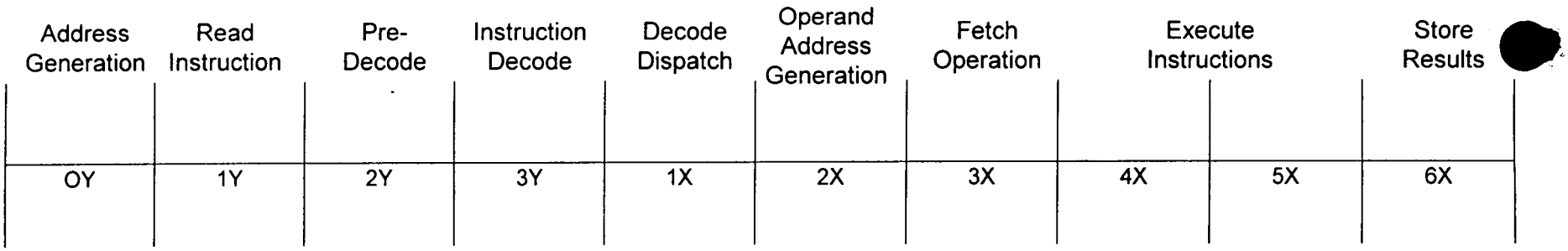


FIG. 1

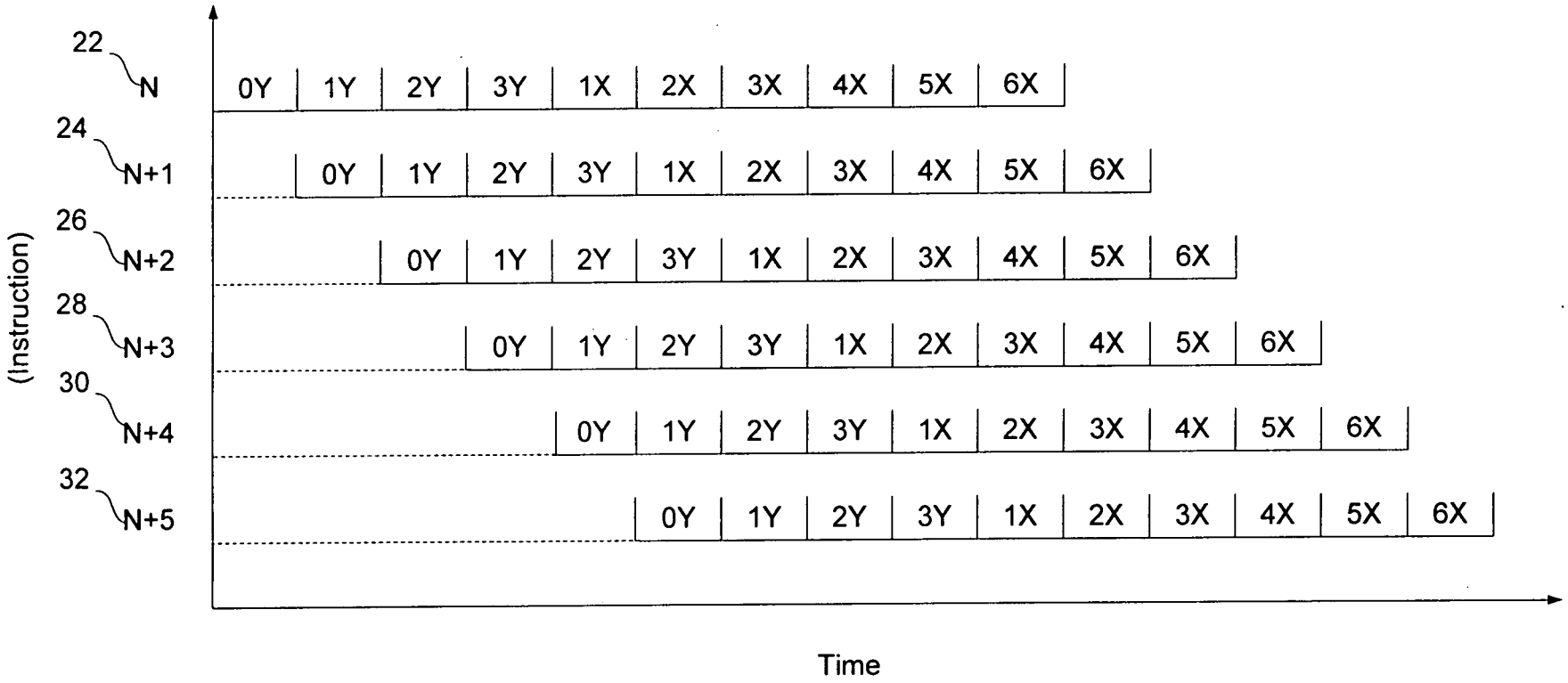

22
N
0Y 1Y 2Y 3Y 1X 2X 3X 4X 5X 6X
24
N+1
0Y 1Y 2Y 3Y 1X 2X 3X 4X 5X 6X
26
N+2
0Y 1Y 2Y 3Y 1X 2X 3X 4X 5X 6X
28
N+3
0Y 1Y 2Y 3Y 1X 2X 3X 4X 5X 6X
30
N+4
0Y 1Y 2Y 3Y 1X 2X 3X 4X 5X 6X
32
N+5
0Y 1Y 2Y 3Y 1X 2X 3X 4X 5X 6X
(Instruction)
Time

FIG. 2

0070271 "h22260

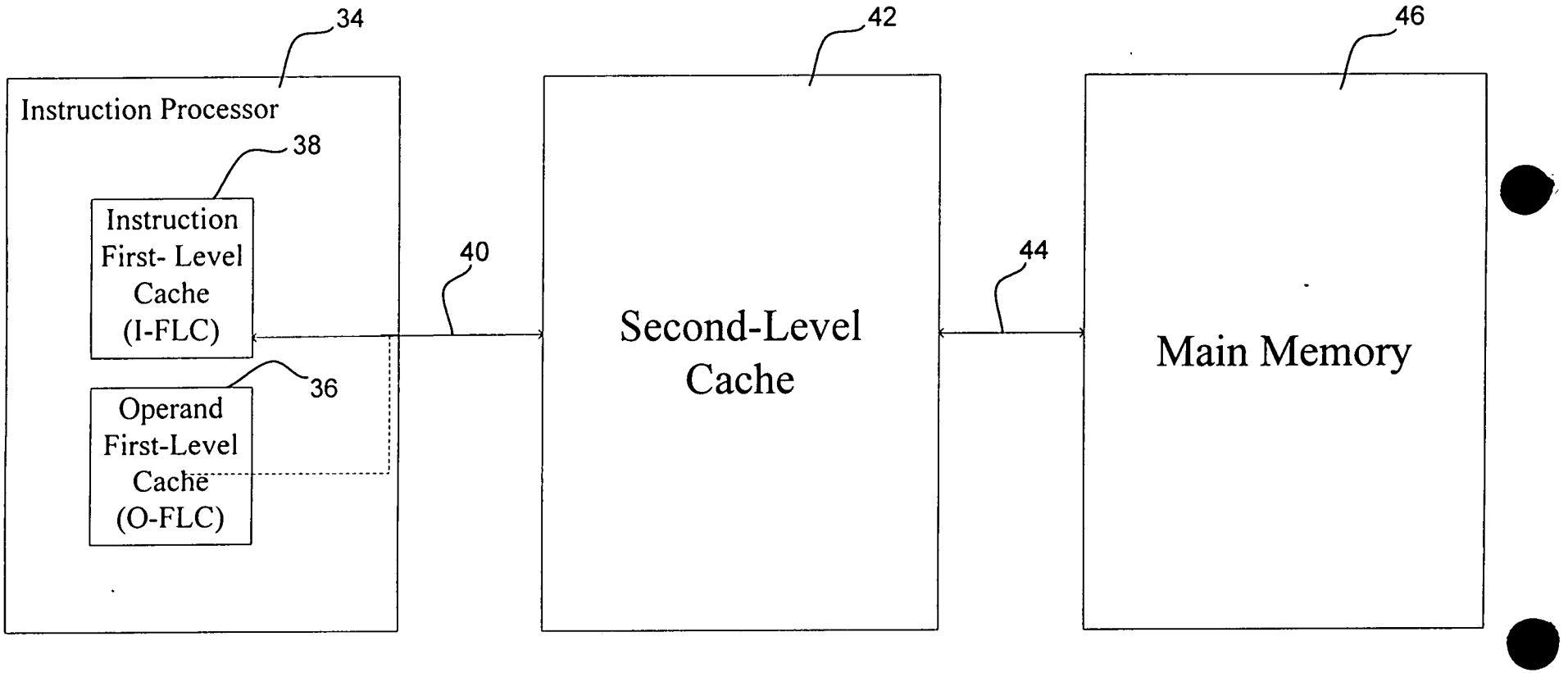


FIG. 3

DOTDET" th22260

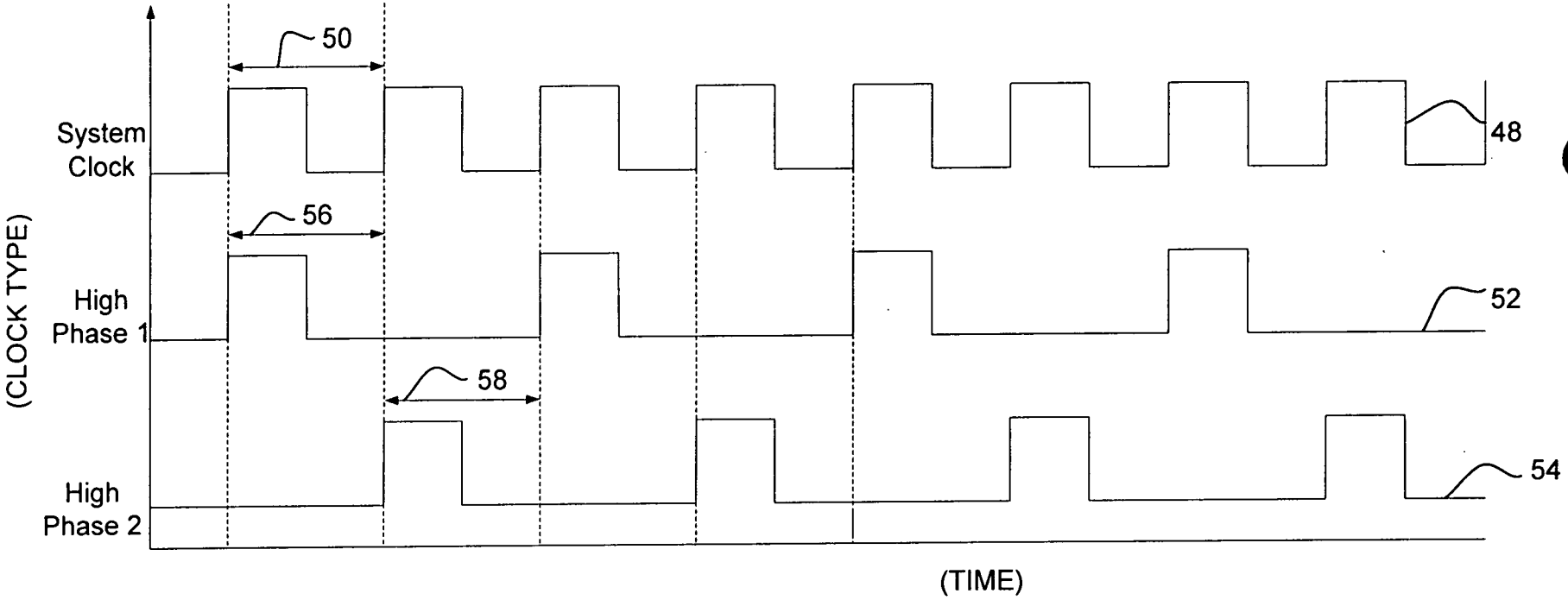


FIG. 4

DOT02T "th22260

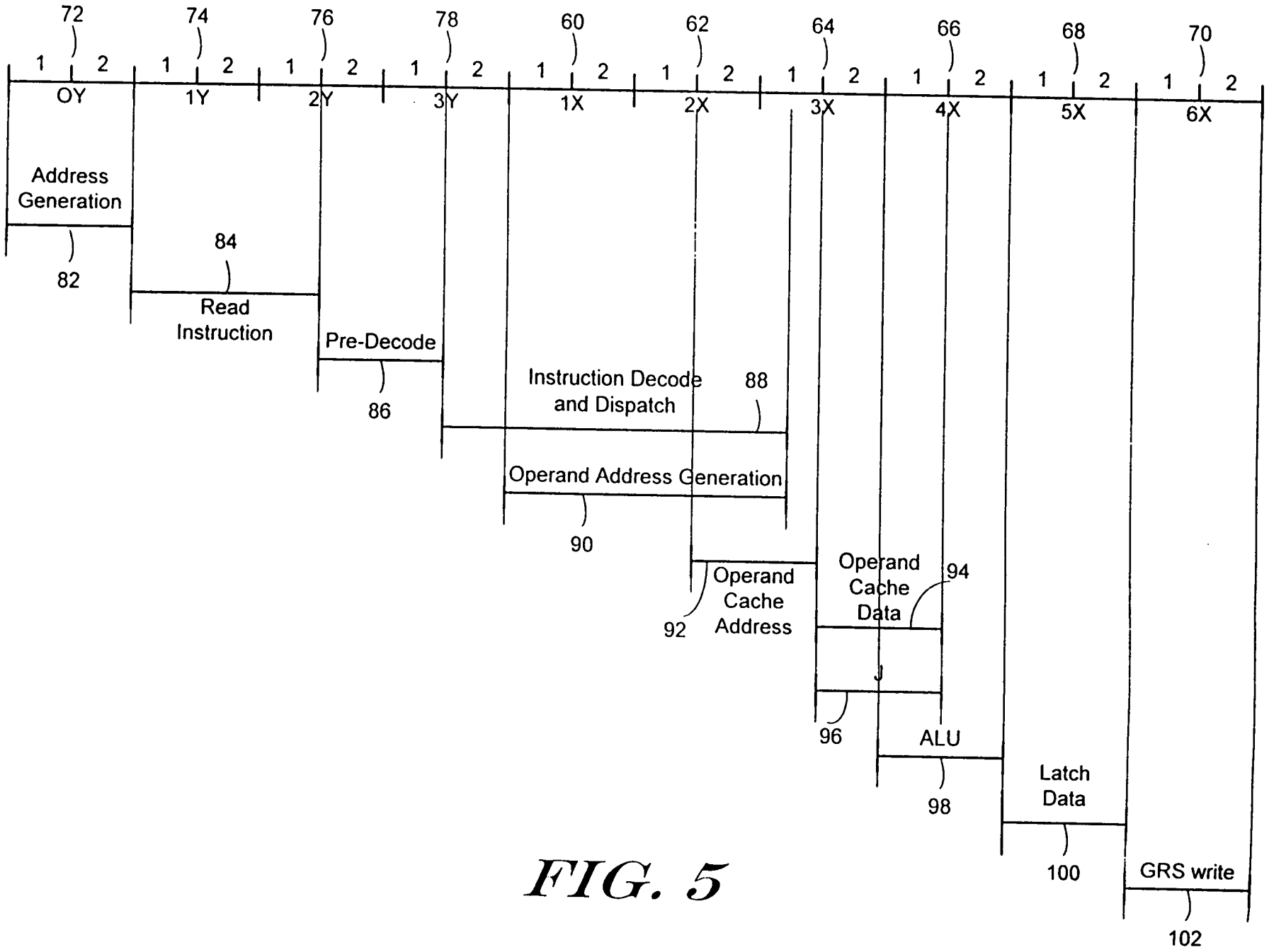


FIG. 5

DOCKET #22260

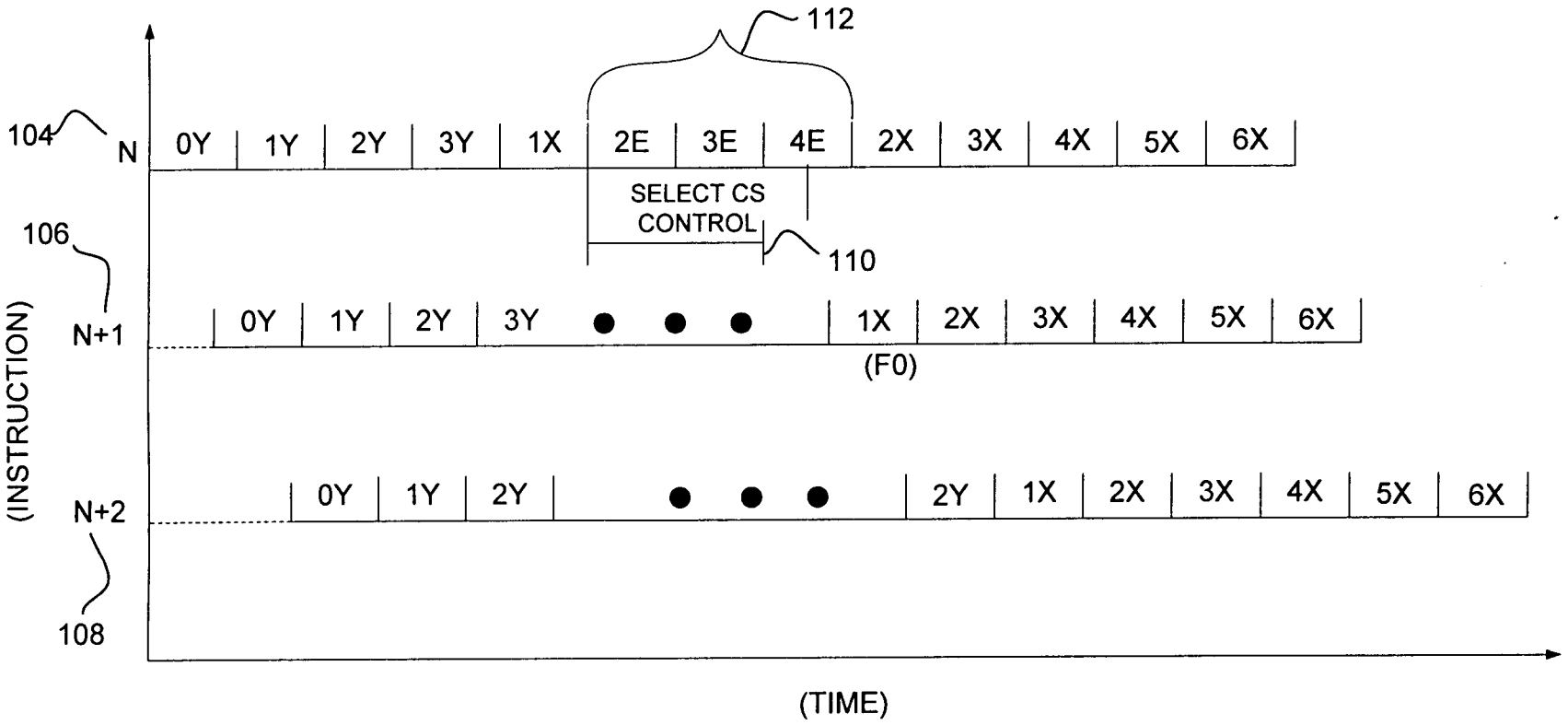


FIG. 6

DOT02T-US2260

FIG. 7

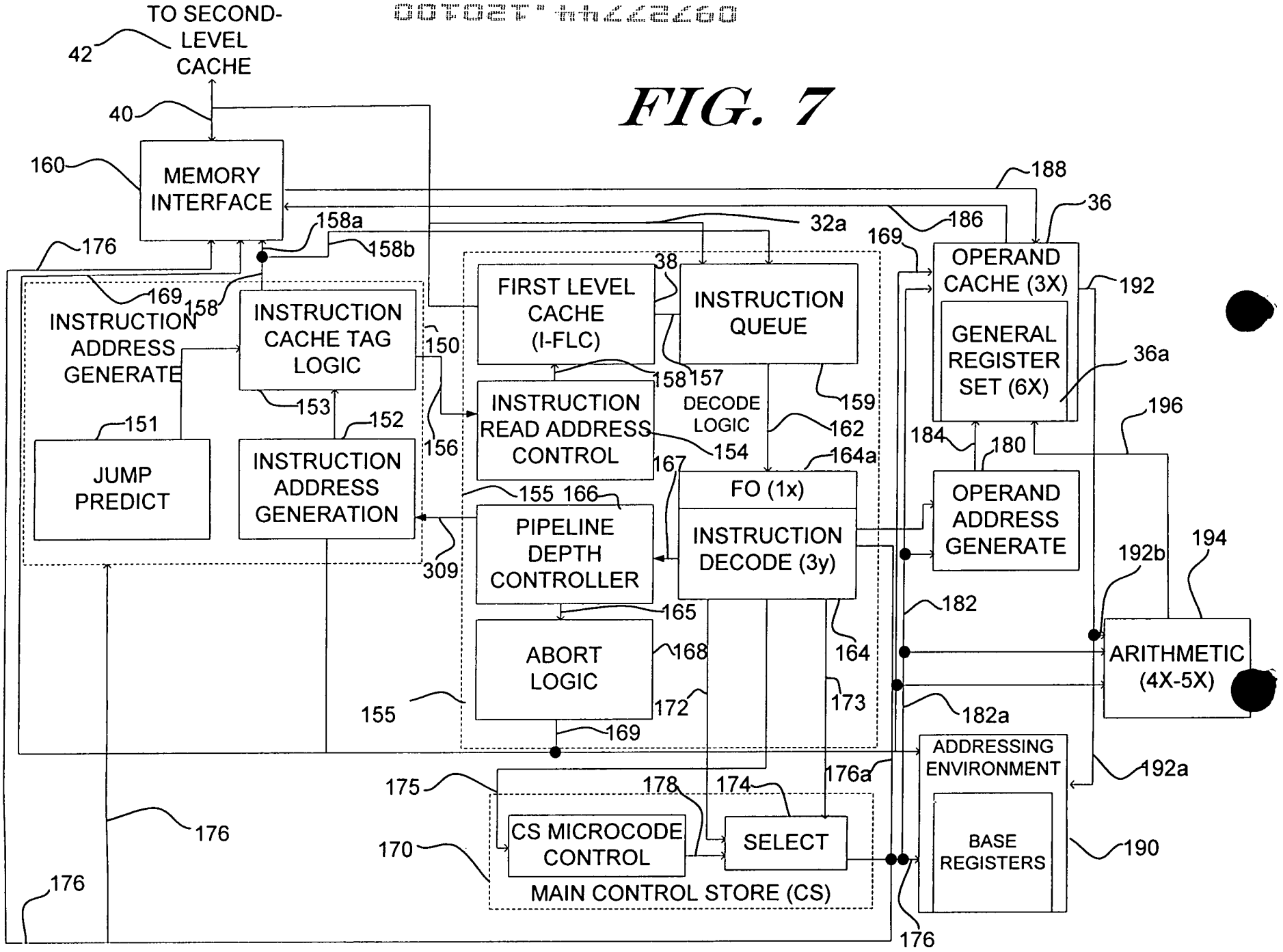
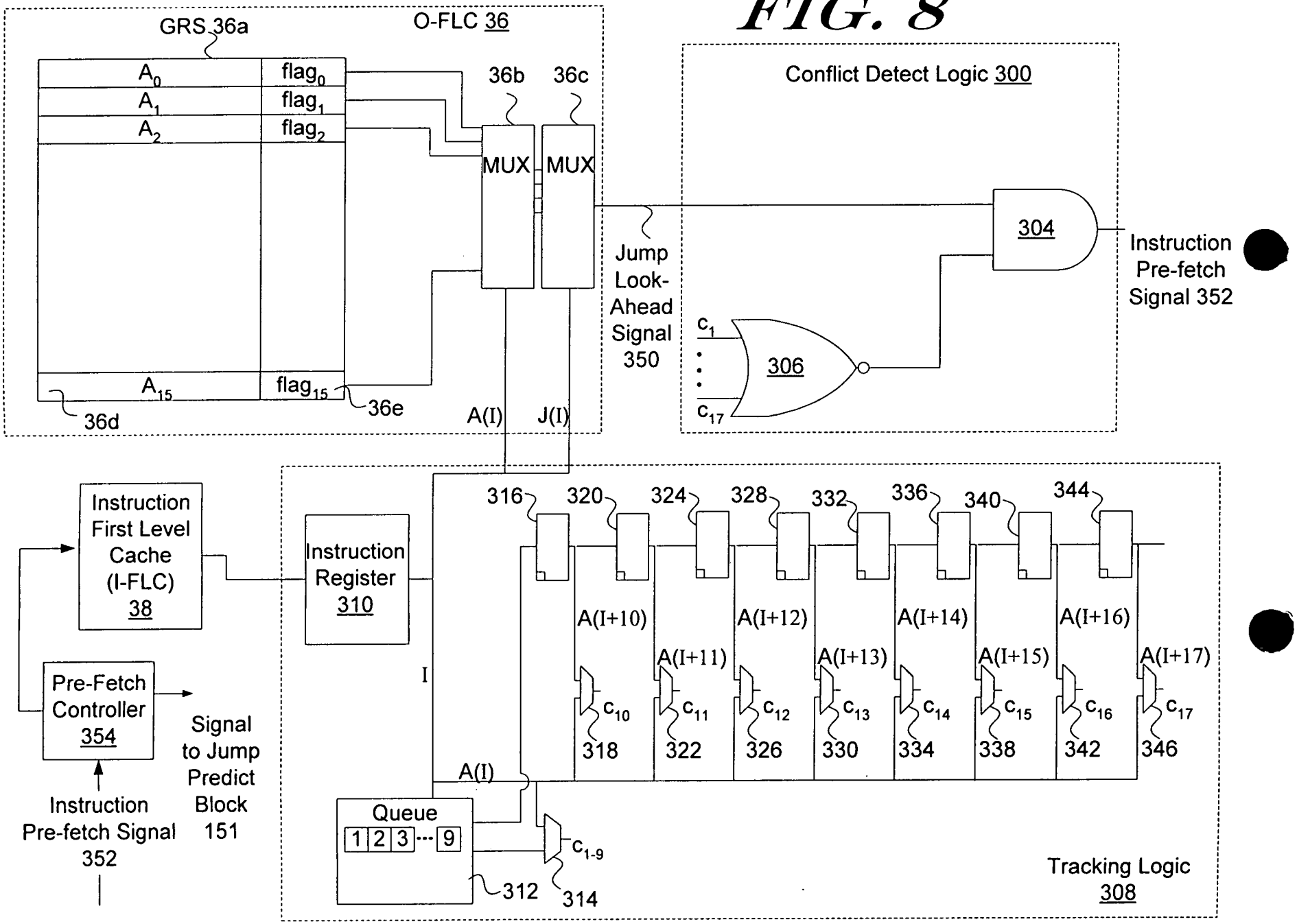


FIG. 8


GRS 36a
O-FLC 36
A_0
A_1
A_2
A_{15}
$flag_0$
$flag_1$
$flag_2$
$flag_{15}$
36b
36c
MUX
MUX
36d
36e
A(I)
J(I)
Conflict Detect Logic 300
304
306
c_1
c_{17}
Jump Look-Ahead Signal 350
Instruction Pre-fetch Signal 352
Instruction First Level Cache (I-FLC) 38
Instruction Register 310
Pre-Fetch Controller 354
Signal to Jump Predict Block 151
Instruction Pre-fetch Signal 352
I
316
320
324
328
332
336
340
344
A(I+10)
A(I+11)
A(I+12)
A(I+13)
A(I+14)
A(I+15)
A(I+16)
A(I+17)
c_{10}
c_{11}
c_{12}
c_{13}
c_{14}
c_{15}
c_{16}
c_{17}
318
322
326
330
334
338
342
346
A(I)
Queue
1 2 3 ··· 9
c_{1-9}
312
314
Tracking Logic 308

08T02T" th22260

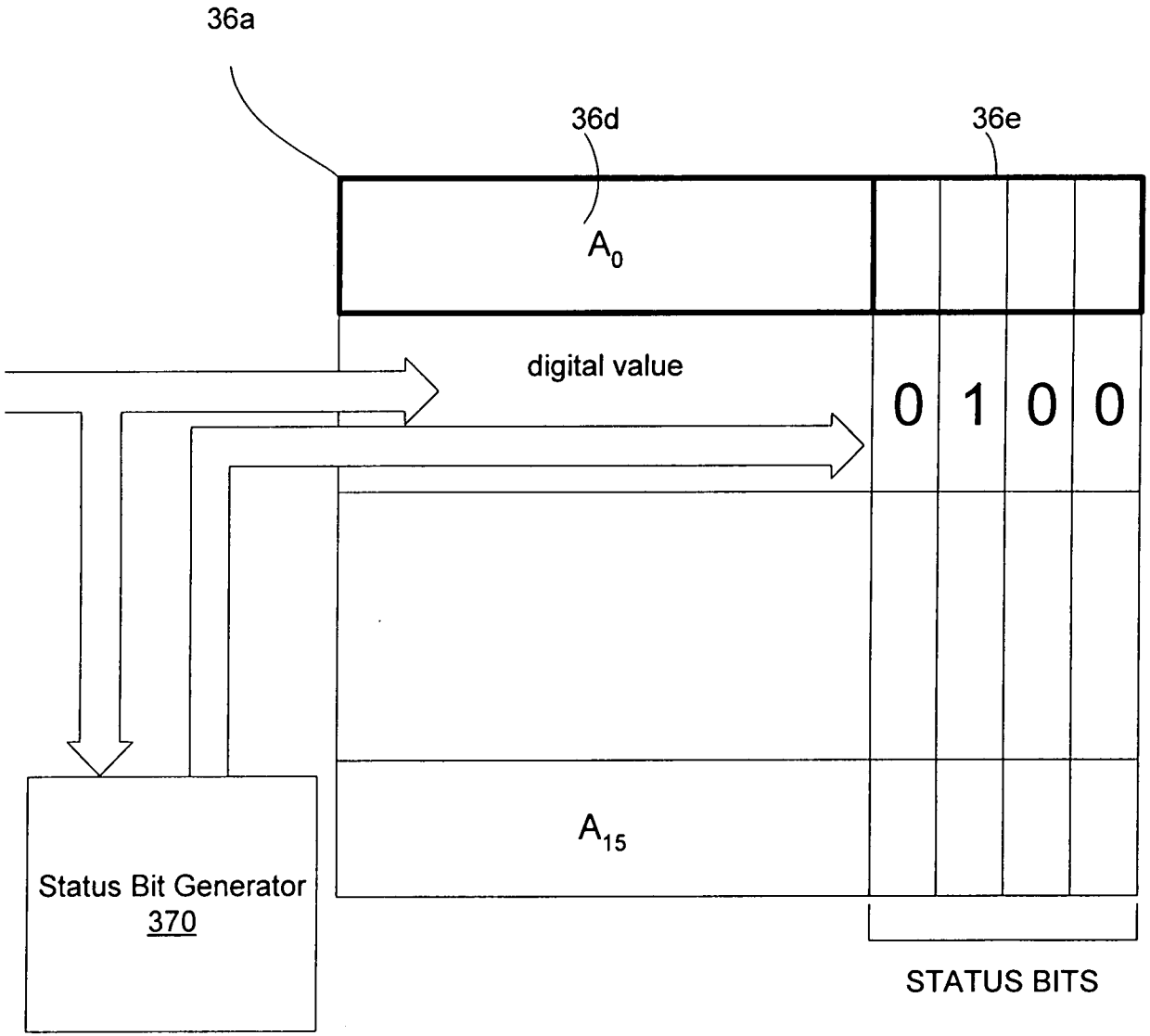


FIG. 9